

INTEL CORP  
Form 4/A  
May 01, 2009

**FORM 4**

**UNITED STATES SECURITIES AND EXCHANGE COMMISSION  
Washington, D.C. 20549**

OMB APPROVAL

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Check this box if no longer subject to Section 16. Form 4 or Form 5 obligations may continue. See Instruction 1(b).

**STATEMENT OF CHANGES IN BENEFICIAL OWNERSHIP OF SECURITIES**

Filed pursuant to Section 16(a) of the Securities Exchange Act of 1934, Section 17(a) of the Public Utility Holding Company Act of 1935 or Section 30(h) of the Investment Company Act of 1940

(Print or Type Responses)

1. Name and Address of Reporting Person \*  
SODHANI ARVIND

(Last) (First) (Middle)  
INTEL CORPORATION, 2200  
MISSION COLLEGE BLVD.  
  
(Street)

SANTA CLARA, CA 95054

(City) (State) (Zip)

2. Issuer Name and Ticker or Trading Symbol  
INTEL CORP [INTC]

3. Date of Earliest Transaction  
(Month/Day/Year)  
04/16/2009

4. If Amendment, Date Original Filed(Month/Day/Year)  
04/20/2009

5. Relationship of Reporting Person(s) to Issuer

(Check all applicable)

Director  10% Owner  
 Officer (give title below)  Other (specify below)  
EXEC. VP; PRES. INTEL CAPITAL

6. Individual or Joint/Group Filing(Check Applicable Line)  
 Form filed by One Reporting Person  
 Form filed by More than One Reporting Person

**Table I - Non-Derivative Securities Acquired, Disposed of, or Beneficially Owned**

1. Title of Security (Instr. 3)	2. Transaction Date (Month/Day/Year)	2A. Deemed Execution Date, if any (Month/Day/Year)	3. Transaction Code (Instr. 8)	4. Securities Acquired (A) or Disposed of (D) (Instr. 3, 4 and 5)	5. Amount of Securities Beneficially Owned Reported Transaction(s) (Instr. 3 and 4)	6. Ownership Form: Direct (D) or Indirect (I) (Instr. 4)	7. Nature of Ownership Indirect Beneficial Ownership (Instr. 4)		
				(A) or (D)	Code	V	Amount	(D)	Price

Reminder: Report on a separate line for each class of securities beneficially owned directly or indirectly.

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SEC 1474  
(9-02)

**Table II - Derivative Securities Acquired, Disposed of, or Beneficially Owned (e.g., puts, calls, warrants, options, convertible securities)**

1. Title of Derivative Security (Instr. 3)	2. Conversion or Exercise	3. Transaction Date (Month/Day/Year)	3A. Deemed Execution Date, if any	4. Transaction Code	5. Number of Derivative Securities	6. Date Exercisable and Expiration Date (Month/Day/Year)	7. Title and Underlying (Instr. 3 and 4)
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	Price of Derivative Security	(Month/Day/Year)	(Instr. 8)	Acquired (A) or Disposed of (D) (Instr. 3, 4, and 5)		Date Exercisable	Expiration Date	Title
				Code	V			
Performance-based Restricted Stock Units	\$ 0 <sup>(1)</sup>	04/16/2009	A		104,350 <sup>(2)</sup>	05/16/2012 <sup>(3)</sup>	<sup>(3)</sup>	Common Stock

## Reporting Owners

Reporting Owner Name / Address	Relationships			
	Director	10% Owner	Officer	Other
SODHANI ARVIND INTEL CORPORATION 2200 MISSION COLLEGE BLVD. SANTA CLARA, CA 95054			EXEC. VP; PRES. INTEL CAPITAL	

## Signatures

/s/ Lulu De Guia,  
attorney-in-fact

04/30/2009

\*\*Signature of Reporting Person                      Date

## Explanation of Responses:

- \* If the form is filed by more than one reporting person, *see* Instruction 4(b)(v).
  - \*\* Intentional misstatements or omissions of facts constitute Federal Criminal Violations. *See* 18 U.S.C. 1001 and 15 U.S.C. 78ff(a).
- Each Performance-based Restricted Stock Unit (RSU) represents the right to receive, following vesting, no less than 33% and no more than 200% of one share of Intel common stock, together with dividend equivalent shares on the vested number of shares. The resulting number of shares of Intel common stock acquired upon vesting of the Performance-based RSUs is contingent upon the achievement of pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year period beginning on the grant date and ending on the third anniversary of the grant date, unless that date falls on a date that the NASDAQ Stock Market is closed, in which case the next business date that the NASDAQ Stock Market is open shall apply.
- (1) Each Performance-based Restricted Stock Unit (RSU) represents the right to receive, following vesting, no less than 33% and no more than 200% of one share of Intel common stock, together with dividend equivalent shares on the vested number of shares. The resulting number of shares of Intel common stock acquired upon vesting of the Performance-based RSUs is contingent upon the achievement of pre-established performance metrics, as approved by the Company's Compensation Committee, over a three-year period beginning on the grant date and ending on the third anniversary of the grant date, unless that date falls on a date that the NASDAQ Stock Market is closed, in which case the next business date that the NASDAQ Stock Market is open shall apply.
  - (2) The number of Performance-based Restricted Stock Units has been reduced from the number originally reported on the Form 4 filed April 20, 2009, following completion of a definitive valuation of the award subsequent to that date.
  - (3) Unless earlier forfeited under the terms of the Performance-based RSU, each Performance-based RSU vests and converts into no less than 33% and no more than 200% of one share of Intel common stock three years and one month after the grant date (together with dividend equivalent shares thereon), unless that date falls on a non-business date, in which case the next business date shall apply.

Note: File three copies of this Form, one of which must be manually signed. If space is insufficient, *see* Instruction 6 for procedure. Potential persons who are to respond to the collection of information contained in this form are not required to respond unless the form displays a currently valid OMB number.